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TO: Examiner Ayal I. Sharon
Commissioner for Patents

DATE: February 15, 2005

FEB 15 2005

FROM: Russell C. Scott

PAGES INCLUDING COVER: 7

RE: Application Serial No. 09/888,474

FAX NO.: 703-872-9306

OUR FILE NO.: 956.1058

IBM REF NO.: AUS9-2000-0049-US1

MESSAGE:

**RE: SERIAL NO. 09/888,474
GROUP ART UNIT 2123**

**ATTACHED PLEASE FIND
RESPONSE TO OFFICE ACTION MAILED NOVEMBER 18, 2004**


**PLEASE DELIVER TO
EXAMINER AYAL I. SHARON**

Thank you!

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I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, (Fax No. 703-872-9306) on February 15, 2005.

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AUS9-2001-0049-US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:
Matthew J. Amatangelo et al.

Serial No.: 09/888,474

Filed: June 25, 2001

FOR: METHOD OF UTILIZING TIMING
MODELS TO PROVIDE DATA FOR STATIC
TIMING ANALYSIS OF ELECTRONIC
CIRCUITS

Group Art Unit: 2123

Examiner: Ayal I. Sharon

Via Facsimile No. 703-872-9306

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CENTRAL FAX CENTER**FEB 15 2005**Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450**RESPONSE TO OFFICE ACTION**

This paper is filed in response to the Office Action mailed November 18, 2004, in the
above-identified patent application and is filed within the three-month shortened statutory period
for response set in the Office Action.